

3005050029

Marine Master Clock with Network Time Server, 70000L

123378-11 Marine Master Clock 70000L With Network Time Server



Description

- ✓ Accuracy +/- 0.1 sec./24 hours
- ✓ Wide range of different slave clocks (digital, 65, Ex-proof.)
- ✓ Easy cabling and calibration
- ✓ Accessories and options like radio- or satellite
- ✓ Signal receivers, RS232C/RS485 interface
- ✓ Alarm outputs, etc.
- ✓ Automatic summer- winter time change-over
- ✓ LAN connection (70000L)

Marine Master Clock is the ideal solution for distribution of both Local and UTC time on board ships.

The Master Clock is equipped with several outputs and inputs for control of Slave Clocks as well as distribution of time to computers and other equipment needing correct time. The four Slave Clock outputs can be individually programmed for different types of clocks. External radio receivers / time synchronisation sources can be connected when higher accuracy is needed.

For control and regulation of various energy consumers such as electrical striking plates, buzzers for pause signalling. The master Clock has a built in yearly programmer with two relay outputs.

The Master Clock is equipped with 10 buttons and a 2 x 16 character LCD. To facilitate the change of time zone two of the buttons are dedicated for this purpose. A light dimmer makes it possible to adjust the background-illumination to the surrounding light level.

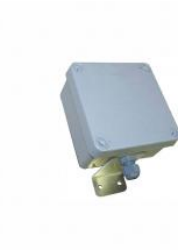
Technical Dimensions

Specifications

GENERAL

Crystal frequency	T4,915200 MHz
Accuracy	0,1 sek./24 hours (+20C°)
Microprocessor	HD6412394
Time memory	30 days (Back-up with Super Capacitor)
Impulse memory	72 hours
Ambient temperature	0° C to +50°C
Relative humidity	Max. 85% non-condensing
Case	19" case according to drawing 084477-00, material aluminium. H=3U
IP class	IP-20
Weight	5.6 kg
CE-approval, EMC	Emission according to EN61000-6-3, Immunity according to EN61000-6-2
Supply voltage	90-264VAC 50/60Hz (Standard) / 24V DC
Power Consumption	65W (max)

Accessories



122980-60 GPS/GNSS receiver

Master Clock System